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Product	Version	Applicable License	Applicable License Guide	Applicable Third Party Terms Guide
<b>QNX Momentics Development Suite</b>				
	6.3.2	<a href="#">CSLA.001</a>	<a href="#">License Guide 2.0</a>	<a href="#">Third Party Terms Guide 2.0</a>
	6.3.0 SP3	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
	6.3.0 SP2 with updates for 6.3.x components	<a href="#">meula1_05c</a>	<a href="#">QDSLG1_05c</a>	<a href="#">TPLTG1_05c</a>
	6.3.0 SP2	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
	6.3.0 SP 1	<a href="#">meula1_05a</a>	<a href="#">QDSLG1_05a</a>	<a href="#">TPLTG1_05a</a>
	6.3.0	<a href="#">meula_1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>
<b>Technology Development Kits</b>				
Acoustic Processing TDK	1.0.0	<a href="#">Meula1_05e</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
Advanced Graphics TDK	2.0	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
Advanced Graphics TDK	1.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Adaptive Partitioning TDK	1.0.0	<a href="#">meula1_05c</a>	<a href="#">QDSLG1_05c</a>	<a href="#">TPLTG1_05c</a>
Critical Process Monitoring TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Extended Networking TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Flash File System and Embedding TDK	1.1.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Flash File System and Embedding TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Instant Device Activation TDK	1.0.0	<a href="#">meula1_05c</a>	<a href="#">QDSLG1_05c</a>	<a href="#">TPLTG1_05c</a>
MOST Development TDK	2.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Multi-Core TDK	1.1.0	<a href="#">meula1_05c</a>	<a href="#">QDSLG1_05c</a>	<a href="#">TPLTG1_05c</a>
Multimedia Framework TDK	1.1.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Multimedia Framework TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Symmetric Multi-Processing TDK	1.0.2	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Symmetric Multi-Processing TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Web Browser TDK	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
Web Browser TDK	6.3.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
<b>Driver Development Kits</b>				
Audio Driver Development Kit	6.3.0	<a href="#">meula_1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>
Character Driver Development Kit	6.3.0 SP1	<a href="#">meula1_05a</a>	<a href="#">QDSLG1_05a</a>	<a href="#">TPLTG1_05a</a>
Graphics Driver Development Kit	6.3.0	<a href="#">meula_1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>
Input Driver Development Kit	6.3.0	<a href="#">meula_1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>
Network Drive Development Kit	6.3.0	<a href="#">meula_1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>

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USB Driver Development Kit	6.3.0	<a href="#">meula 1_05</a>	<a href="#">QDSL1_05</a>	<a href="#">TPLTG1_05</a>
<b>Board Support Packages</b>				
BSP for AMD Geode LX DB880	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSL1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for AMCC PPC440 1.0.0	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSL1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for ARM Integrator *	6.3.x	<a href="#">meula 1_05</a>	<a href="#">QDSL1_05</a>	<a href="#">TPLTG1_05</a>
BSP for Broadcom BCM12xx and BCM14xx	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Broadcom BCM91250 and BCM91125	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Broadcom BCM91x80 BSP *	1.0.0	<a href="#">meula1_05c</a>	<a href="#">QDSL1_05c</a>	<a href="#">TPLTG1_05c</a>
BSP for Centrality Atlas II BSP	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSL1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for Freescale CDS MPC8541	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale CDS MPC85xx BSP	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale CDS MPC85xx BSP *	1.0.1	<a href="#">meula1_05c</a>	<a href="#">QDSL1_05c</a>	<a href="#">TPLTG1_05c</a>
BSP for Freescale i.MX31 BSP	1.0.0	<a href="#">meula1_05c</a>	<a href="#">QDSL1_05c</a>	<a href="#">TPLTG1_05c</a>
BSP for Freescale Lite5200B and Media5200	1.0.1	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Freescale Lite5200B and Media5200	1.0.2	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale Lite5200B and Media5200	1.0.3	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale Lite5200B and Media5200 *	1.0.4	<a href="#">meula1_05c</a>	<a href="#">QDSL1_05c</a>	<a href="#">TPLTG1_05c</a>
BSP for Freescale Lite5200B	1.0.3	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale Media5200	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale MPC8260 ADS	6.3.x	<a href="#">meula 1_05</a>	<a href="#">QDSL1_05</a>	<a href="#">TPLTG1_05</a>
BSP for Freescale MPC8349 MDS	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale MPC8360E MDS	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale MPC85x0 ADS	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale MPC85x0 ADS	1.0.2	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Freescale PQ2FADS	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale PQ2FADS	1.0.2	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Freescale Sandpoint	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Freescale Total5200 and Lite5200EVB	1.0.1	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Freescale Total5200 and Lite5200EVB	1.0.2	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for IBM PPC405	1.0.0	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for IBM PPC440	1.0.0	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Intel DBPXA250DP	6.3.x	<a href="#">meula 1_05</a>	<a href="#">QDSL1_05</a>	<a href="#">TPLTG1_05</a>
BSP for Intel DBPXA270DP	1.0.0	<a href="#">meula1_05a</a>	<a href="#">QDSL1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Intel IXDP2351 ADP	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Intel IXDP2800 (ARM)	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Intel IXDP2800 (ARM)	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Intel IXDP425	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Intel IXDP465	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSL1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Marvell MV64460	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSL1_05d</a>	<a href="#">TPLTG1_05d</a>

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BSP for NEC Malta	6.3.x	<a href="#">meula 1_05</a>	<a href="#">QDSLG1_05</a>	<a href="#">TPLTG1_05</a>
BSP for PMC-Sierra RM9150	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Renesas Big Sur-Amanda	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Renesas BigSur Amanda (SH4)	1.0.0	<a href="#">meula1_05a</a>	<a href="#">QDSLG1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Renesas Biscayne	1.0.1	<a href="#">meula1_05a</a>	<a href="#">QDSLG1_05a</a>	<a href="#">TPLTG1_05a</a>
BSP for Renesas EDOSK 7780	1.0.0	<a href="#">meula1_05c</a>	<a href="#">QDSLG1_05c</a>	<a href="#">TPLTG1_05c</a>
BSP for Renesas EDOSK7780 *	1.1.0	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for Renesas Lanbic	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Renesas Lanbic	1.1.0	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for Renesas SDK7785	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for Renesas Sequoia7397	1.0.0	<a href="#">Meula1_05d</a>	<a href="#">QDSLG1_05d</a>	<a href="#">TPLTG1_05d</a>
BSP for Renesas SystemH	1.0.1	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for TI DaVinci	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for TI OMAP 5905 OSK	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for TI OMAP 5912 OSK	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for X86-BIOS *	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Xilinx Virtex-4 ML403	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>
BSP for Xilinx Virtex-II Pro ML300	1.0.0	<a href="#">meula1_05b</a>	<a href="#">QDSLG1_05b</a>	<a href="#">TPLTG1_05b</a>

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<a href="#">meula 1_04</a>	<a href="#">QDSLG1_04</a>	<a href="#">TPOSLTG1_04</a>
<a href="#">meula 1_03</a>	<a href="#">QDSLG1_03</a>	<a href="#">TPOSLTG1_03</a>
<a href="#">meula 1_02</a>	<a href="#">QDSLG1_02</a>	<a href="#">TPOSLTG1_02</a>
<a href="#">meula 1_01</a>	<a href="#">QDSLG1_01</a>	<a href="#">TPOSLTG1_01</a>
<a href="#">meula 1_00</a>	<a href="#">QDSLG1_00</a> and <a href="#">QDSLG 1_00A</a>	<a href="#">TPOSLTG1_00</a> and <a href="#">TPOSLTG1_00A</a>
<a href="#">nceula 1_01</a>	<a href="#">QDSLG1_03</a>	<a href="#">TPOSLTG1_03</a>
nceula 1_00	<a href="#">QDSLG1_00</a> , <a href="#">QDSLG 1_00A</a> , <a href="#">QDSLG1_01</a> and <a href="#">QDSLG1_02</a>	<a href="#">TPOSLTG1_00</a> , <a href="#">TPOSLTG1_00A</a> , <a href="#">TPOSLTG1_01</a> and <a href="#">TPOSLTG1_02</a>
<a href="#">gdeula 1_04</a>	N/A	N/A
<a href="#">gdeula 1_03</a>	N/A	N/A
<a href="#">gdeula 1_02</a>	N/A	N/A
<a href="#">gdeula 1_01</a>	N/A	N/A
<a href="#">gdeula 1_00</a>	N/A	N/A
<a href="#">rtpeula 1_10</a>	N/A	N/A

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